

AB Logix pds driver V2.0

Allen-Bradley CompactLogix: 1769-L20, 1769-L30, 1769-L31, 1769-L32E, 1769-L35E
<http://www.ab.com>

HMI Setting:

Parameters	Recommend	Option	Notes
PLC type	AB Logix DF1		
Com port	RS232		
Baud rate	19200	9600, 19200, 38400	
Parity bit	None	Even, Odd, None	
Data Bits	8	8	
Stop Bits	1	1	
HMI Station No.	0		
PLC Station No.	1	1-31	

PLC Setting:

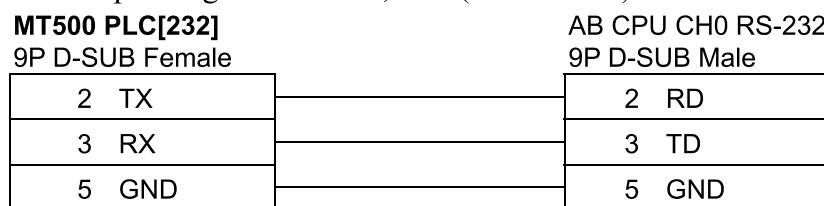
Communication mode	DF1 Full Duplex protocol 19200, None, 8, 1 (default)
	Error Check: BCC, Station Address: 1

Device address:

Bit/Word	Device Type	Format	Range	Memo
B	B BOOL	ffddd(dd)	File no. ff: 3, 10~64 Element no. ddd: 0~255 Bit no. (dd): 0~15	Bit data file
B	N BOOL	ffddd(dd)	File no. ff: 7, 10~64 Element no. ddd: 0~255 Bit no. (dd): 0~15	Integer data file bit level (N7, 10~64)
W	Bx INT	ffffdd	File no. fff: 3, 10~64 Element no. ddd: 0~255	Bit data file word level
DW	Tx.PRE	ffffdd	File no. fff: 4, 10~64 Element no. ddd: 0~255	Timer Preset Value (T4, T10~64)
DW	Tx.ACC	ffffdd	File no. fff: 4, 10~64 Element no. ddd: 0~255	Timer Accumulator Value (T4, T10~64)
DW	Cx.PRE	ffffdd	File no. fff: 5, 10~64 Element no. ddd: 0~255	Counter Preset Value (C5, C10~64)
DW	Cx.ACC	ffffdd	File no. fff: 5, 10~64 Element no. ddd: 0~255	Counter Accumulator Value (C5, C10~64)
DW	F8 REAL	ddd	ddd:0~255	Floating point data file (F8)
W	Nx INT	ffffdd	File no. fff:0~64 Element no. ddd:0~255	Integer data file (N7, 10~64)

Wiring diagram:

RS-232: CompactLogix CPU CH0, CH1(RS232 DF1)



RSLogix 5000 setting

You can configure a mapping table to allow the controller to accept the PLC-2, 3, 5, or SLC/500 messages.

Configure Mapping for a PLC-3, PLC-5, or SLC/500 Processor

1. From the Logic menu, choose Map PLC Messages.
2. In the Mapping frame, enter the File Number and Tag Name to be mapped.
3. Click on OK to configure the mapping.

